



TFco 2817

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/531,050
Applicant : Shunsuke Hirano et al.
Filed : April 12, 2005
Title : "MODULATOR AND CORRECTION METHOD
THEREOF"

Confirmation No. : 5055
TC/A.U. : 2817
Examiner : TBD

Customer No. : 116
Docket No. : 38028

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, Va. 22313-1450

Sir/Madam:

In accordance with Rule 56, applicants are aware of the publications listed in the European Search Report (ESR, copy enclosed) of a corresponding European application and in the enclosed copy of Patent Office Form 1449. A copy of each of the publications is enclosed herewith.

The undersigned hereby certifies that each item of information contained in this Supplemental Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Supplemental Information Disclosure Statement.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date indicated below.

Jeffrey J. Sopko

Name of Attorney for Applicant(s)

April 11, 2006

Date

Signature of Attorney

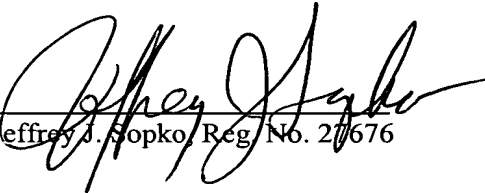
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If there are any fees required by this communication, please charge such fees to our
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Respectfully submitted,

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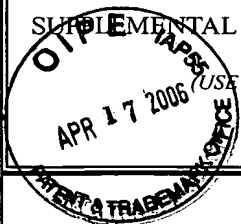
APPLICANT:

Shunsuke Hirano et al.

FILING DATE:

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GROUP ART UNIT:

2817

U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Date	Name	Class	Subclass	Filing Date If Appropriate
	A						
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	D						
	E						
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	I						
	J						
	K						

FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Class	Subclass	Translation
	L	2 809 890	12/2001	FRANCE			Cited on ESR-English Abstract
	M						
	N						
	P						
	Q						

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

	R	Perrott et al., "A 27-mW CMOS Fractional -N Synthesizer Using Digital Compensation for 2.5-Mb/s GFSK Modulation, 12/1997, pages 2048-2060 (Cited on ESR-English Text)
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